



# workshop in memory *of Ken Kennedy*

## Workshop in Memory of Ken Kennedy Speaker Bios

### **Preston Briggs** -- *Senior Software Engineer, Google*

Preston Briggs finished a PhD in Computer Science in 1992. His adviser was Keith Cooper; thus, he was one of Ken's grandstudents. His thesis was on register allocation, as was Ken's. Preston's first job after Rice was with Tera Computer, where Ken was on the Board of Directors. After 11 years at Tera and Cray, he's now at Google. Thanks to Ken's extensive publishing, he has an Erdos number of 3.

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### **Mary Hall** -- *Project Leader, Information Sciences Institute, and Associate Professor, Computer Science, University of Southern California*

Dr. Mary Hall is currently a Project Leader at Information Sciences Institute and jointly a Research Associate Professor in Computer Science at University of Southern California, where she has been since 1996. Her research focuses on compiler support for high performance, targeting a variety of architectures from Field-Programmable Gate Arrays (FPGAs) and multimedia extension architectures, to shared-memory multiprocessors and memory hierarchies of high-end systems.

Dr. Hall has published over 70 articles and served on over 40 program committees in compilers and their interaction with architecture, parallel computing, and embedded and reconfigurable computing, including the 2005 program chair of the ACM SIGPLAN PLDI conference, poster chair of SC'05, and workshop co-chair for SC'07 and SC'08.

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### **Uli Kremer** -- *Associate Professor, Computer Science, Rutgers University*

Ulrich Kremer is an Associate Professor in the Department of Computer Science at Rutgers University. He received his PhD and MS in computer science from Rice University in 1995 and 1993, respectively, under the supervision of Ken Kennedy. His research interests include programming environments and advanced optimizing compilers for imperative (Fortran, C), object oriented (Java), and parallel languages (HPF). He has investigated compiler-directed techniques to reduce the power dissipation and energy consumption of programs, in particular reductions in CPU and disk power/energy. More recently, he has worked on new programming abstractions and compiler optimizations for location-aware and resource-aware applications for hybrid networks of mobile and stationary devices. Ulrich has received an NSF CAREER award to support his low power/energy compiler work. In addition, he has been the PI and Co-PI of several other projects funded by NSF or DARPA.



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**Kathryn McKinley** -- *Professor, Computer Science, University of Texas at Austin*

Kathryn S. McKinley is a Professor at the University of Texas at Austin. Her research interests include compilers, runtime systems, and architecture. Her research seeks to enable high level programming languages to achieve high performance, reliability, and availability. She and her collaborators have developed compiler optimizations for improving memory system performance, high performance garbage collection algorithms, scalable explicit memory management algorithms for parallel systems, and cooperative dynamic optimizations for improving the performance of managed languages. She collaborates on the compiler effort for the deployed TRIPS system, which is exploring technology scalability using Explicit Dataflow Graph Execution (EDGE) architectures. Her honors include ACM Distinguished Scientist and an NSF CAREER Award. She is currently an Editor-in-Chief of the ACM Transactions on Programming Language and Systems (TOPLAS) and served as the program chair for the ACM Conference on Programming Language Design and Implementation (PLDI 2007). Her service activities include increasing minority participation in computer science. For example, she organized with Daniel Jimenez the CRAW/CDC Programming Languages Summer School, 2007. She has published over 75 refereed articles and has supervised 8 PhD degrees. Prof. McKinley holds a BA (1985) in electrical engineering and computer science, and an MS (1990) and PhD supervised by Ken Kennedy (1992) in computer science, all from Rice University.

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**Apan Qasem** -- *Assistant Professor, Computer Science, Texas State University*

Apan Qasem is an Assistant Professor at Texas State University. He received his PhD in Computer Science from Rice University in 2007. Before coming to Rice he earned a Masters in Computer Science from Florida State University and a BA in Computer Science and Economics from Ohio Wesleyan University. His native country is Bangladesh

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**F. Kenneth Zadeck** -- *Founder, Chief Technology Officer, NaturalBridge*

Kenneth Zadeck co-founded NaturalBridge in 1996. He is an expert in Java and has done extensive work regarding the challenges of implementing large server applications in Java. He is also a recognized authority in Static Single Assignment (SSA). At NaturalBridge, Dr. Zadeck led the development of BulletTrain, a state of the art Java compiler and runtime.

Prior to founding NaturalBridge, Dr. Zadeck was a Research Staff Member in the Computer Science Department at IBM T.J. Watson Research Center, where he worked on automatic generation of machine-specific optimizers. From 1986 to 1992 Dr. Zadeck was an Assistant Professor in the Computer Science Department at Brown University. From 1983 to 1986 Dr. Zadeck was a Research Staff Member in the Math Department at IBM T.J. Watson Research Center where he co-developed the Static Single Assignment (SSA) representation used in compiler data flow analysis. Dr. Zadeck is the author of papers on optimization technology, attribute grammar systems, incremental computation, and programming environments. He received his Ph.D., M.S. and B.S. in applied mathematics from Rice University.

Kenneth Zadeck is also the winner (with R. Cytron, J. Ferrante, B. K. Rosen and M. Wegman) of the 2006 ACM SIGPLAN Programming Languages Achievement Award.